◆IEEE

IEEE Xplore®
RELEASE 1.5

Welcome
United States Patent and Trademark Office

Help   FAQ   Terms   IEEE Peer Review   **Quick Links** ▼

» Search Results

Welcome to IEEE *Xplore*

○ **Home**
○ **What Can I Access?**
○ **Log-out**

Tables of Contents

○ **Journals & Magazines**
○ **Conference Proceedings**
○ **Standards**

Search

○ **By Author**
○ **Basic**
○ **Advanced**

Member Services

○ **Join IEEE**
○ **Establish IEEE Web Account**
○ **Access the IEEE Member Digital Library**

🖳 **Print Format**

Your search matched **2** of **987604** documents.

A maximum of **2** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.
You may refine your search by editing the current search expression or entering a new one the text box.
Then click **Search Again**.

| hardware accelerator and java | Search Again |

Results:
Journal or Magazine = **JNL**   Conference = **CNF**   Standard = **STD**

---

1 **Virtual Java/FPGA interface for networked reconfiguration**
Yajun Ha; Vanmeerbeeck, G.; Schaumont, P.; Vemalde, S.; Engels, M.; Lauwereins, R.; De Man, H.;
Design Automation Conference, 2001. Proceedings of the ASP-DAC 2001. Asia and South Pacific , 30 Jan.-2 Feb. 2001
Page(s): 558 -563

[Abstract]   [PDF Full-Text (436 KB)] **IEEE CNF**

---

2 **Adaptive solid texturing for Web3D applications**
Bing-Yu Chen; Nishita, T.;
Computer Graphics and Applications, 2002. Proceedings. 10th Pacific Conference on , 9-11 Oct. 2002
Page(s): 433 -434

[Abstract]   [PDF Full-Text (312 KB)] **IEEE CNF**

◆IEEE

Membership   Publications/Services   Standards   Conferences   Careers/Jobs

IEEE *Xplore*®
RELEASE 1.5

Welcome
United States Patent and Trademark Office

Help   FAQ   Terms   IEEE Peer Review   **Quick Links**   ▼   **» Search Results**

Welcome to IEEE *Xplore*®

○ Home
○ What Can
  I Access?
○ Log-out

Tables of Contents

○ Journals
  & Magazines
○ Conference
  Proceedings
○ Standards

Search

○ By Author
○ Basic
○ Advanced

Member Services

○ Join IEEE
○ Establish IEEE
  Web Account
○ Access the
  IEEE Member
  Digital Library

🖶 Print Format

Your search matched **1** of **989552** documents.

A maximum of **1** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.
You may refine your search by editing the current search expression or entering a new one the text box.
Then click **Search Again**.

hardware accelerator and object-oriented	Search Again

Results:
Journal or Magazine = **JNL**   Conference = **CNF**   Standard = **STD**

1   **Dynamic circuit generation for Boolean satisfiability in an object-oriented design environment**
Mencer, O.; Plazner, M.;
System Sciences, 1999. HICSS-32. Proceedings of the 32nd Annual Hawaii
International Conference on , Volume: Track3 , 5-8 Jan. 1999
Page(s): 8 pp.

[Abstract]   [PDF Full-Text (188 KB)] **IEEE CNF**

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search
Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting
No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ| Terms | Back to Top

Membership   Publications/Services   Standards   Conferences   Careers/Jobs

IEEE Xplore®
RELEASE 1.5

Welcome
United States Patent and Trademark Office

Help   FAQ   Terms   IEEE Peer Review

Quick Links ▼

» Search Results

Your search matched **6** of **987604** documents.

A maximum of **6** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.
You may refine your search by editing the current search expression or entering a new one the text box.
Then click **Search Again**.

hardware accelerator and object | Search Again |

Results:
Journal or Magazine = **JNL**   Conference = **CNF**   Standard = **STD**

---

1 **Dynamic circuit generation for Boolean satisfiability in an object-oriented design environment**
Mencer, O.; Plazner, M.;
System Sciences, 1999. HICSS-32. Proceedings of the 32nd Annual Hawaii
International Conference on , Volume: Track3 , 5-8 Jan. 1999
Page(s): 8 pp.

[Abstract]   [PDF Full-Text (188 KB)] **IEEE CNF**

---

2 **A solution to high performance acceleration of digital system design**
Carlstedt-Duke, T.B.M.;
Hardware Accelerators for VLSI CAD - A Tutorial, IEE Colloquium on , 5 Sep 1988
Page(s): 4/1 -4/9

[Abstract]   [PDF Full-Text (364 KB)] **IEE CNF**

---

3 **Latest benchmark results of VHDL simulation systems**
Rohm, E.;
Design Automation Conference, 1995, with EURO-VHDL, Proceedings EURO-DAC '95.,
European , 18-22 Sept. 1995
Page(s): 406 -411

[Abstract]   [PDF Full-Text (440 KB)] **IEEE CNF**

---

4 **How accurate should be disparities estimated for image-based rendering?**
Izquierdo, E.;
Video/Image Processing and Multimedia Communications 4th EURASIP-IEEE Region 8
International Symposium on VIPromCom , 16-19 June 2002
Page(s): 69 -74

[Abstract]   [PDF Full-Text (1102 KB)] **IEEE CNF**

---

5 **Adaptive solid texturing for Web3D applications**
Bing-Yu Chen; Nishita, T.;
Computer Graphics and Applications, 2002. Proceedings. 10th Pacific Conference on ,
9-11 Oct. 2002
Page(s): 433 -434

[Abstract]   [PDF Full-Text (312 KB)] **IEEE CNF**

---

6 **Face recognition/detection by probabilistic decision-based neural network**
Shang-Hung Lin; Sun-Yuan Kung; Long-Ji Lin;
Neural Networks, IEEE Transactions on , Volume: 8 Issue: 1 , Jan. 1997
Page(s): 114 -132

[Abstract]   [PDF Full-Text (504 KB)] **IEEE JNL**

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search
Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting
No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ| Terms | Back to Top